

GP-431
2811

Docket: 0756-1626

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT Application of)

Art Unit: 1931

Shunpei YAMAZAKI et al.)

Examiner: Wael M. Fahmy

Serial No.: 08/788,560)

Filed: January 24, 1997)

For: SEMICONDUCTOR DEVICE)
AND METHOD FOR FORMING)
THE SAME)

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to: Assistant Commissioner for Patents and Trademarks, Washington, D.C. 20231, on 1-14-2000.

[Signature]

TRANSMITTAL LETTER

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith is an Amendment in the above-identified application.

The fees have been calculated as shown below:

	Claims Remaining After Amendment		Highest Number Previously Paid For	Present Extra	Small/Large Entity Rate	Fee
Total	80	Minus	80		x \$9/\$18	\$0
Indep	13	Minus	13		x \$39/\$78	\$0

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Application No. 08/788,560
Attorney Docket No. 0756-1626

First Presentation of Multiple Dependent Claim	+	\$0
	\$130/\$260	
TOTAL		\$0


[X] No additional claim fee is required.

[X] In the event applicant(s) has overlooked the need for any petition and fee for extension of time, and such extension is required, applicant(s) requests that this be considered a petition therefor and that such fee be charged to Deposit Account No. 19-2380.

[X] The Commissioner is hereby authorized to charge fees under 37 CFR 1.16, 1.17, 1.20(a), 1.20(b), 1.20(c) and 1.20 (d) (except the Issue Fee) which may be required now or hereafter, or credit any overpayment, to Deposit Account No. 19-2380. A duplicate copy of this sheet is attached.

Respectfully submitted,

SIXBEY, FRIEDMAN, LEEDOM & FERGUSON, P.C.

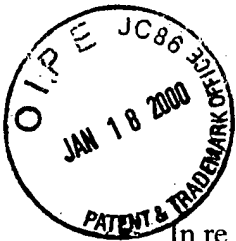


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EJR:RCC/dkt
Attorney Docket No.: 0756-1626

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PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

25/6
2-2-90
Sunder

In re Application of)

Group Art Unit: 1931

Shunpei YAMAZAKI et al.)

Examiner: Wael M. Fahmy

Serial No.: 08/788,560)

Filed: January 24, 1997)

For: SEMICONDUCTOR DEVICE)
AND METHOD FOR FORMING)
THE SAME)

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1-14-2000

[Signature]

AMENDMENT

Assistant Commissioner for Patents
Washington, D. C. 20231

Sir:

In response to the Office Action mailed October 14, 1999, please amend the above-identified application as follows.

IN THE CLAIMS:

Please amend claims 78, 82, 84, 88, 90, 94, 96, 100, 102, 108-110, 116-118, 124-126, 132-134, 138, 140, 144, 146, 150, 152, 156 as follows:

61 78. (Amended) A semiconductor device for electro-optical device comprising a CMOS device comprising n-channel and p-channel TFTs, each of said n-channel and p-channel TFTs comprising:

a semiconductor layer comprising at least a channel region and source and drain regions with said channel region interposed therebetween; and

a gate electrode adjacent to said channel region with a gate insulating film interposed therebetween,